AMENDMENTS TO THE CLAIMS

Claim 1. (Currently Amended)

An image processing unit, comprising:

a frequency dividing device that divides a frequency of a drive clock for an imaging device;

an A/D converting device that converts an image signal outputted from said imaging device according to said drive clock into a digital image data; and

a signal processing part that captures said digital image data outputted from said A/D converting device in synchronization with a frequency-divided clock outputted from said frequency dividing device, and that processes said digital image data,

wherein the image data is thinned out upon ~~the input of the data into~~ application of the frequency-divided clock signal to the signal processing part.

Claim 2. (Original)

The image processing unit as set forth in claim 1, wherein said imaging device is a CCD solid state imaging device whose pixels are arranged in one of G-stripe arrangement and Bayer arrangement.

Claim 3. (Original)

The image processing unit as set forth in claim 1, wherein said frequency dividing device divides the frequency of said drive clock by an odd number.

Claim 4. (Original)

The image processing unit as set forth in claim 2, wherein said frequency dividing device divides the frequency of said drive clock by an odd number.

Claim 5. (Original)

The image processing unit as set forth in claim 1, further comprising a display that displays an image according to an image data outputted from said signal processing part.

Claim 6. (Currently Amended)

An image processing unit, comprising:

an imaging device;

a timing generating device that generates a drive clock for driving said imaging device;

a frequency dividing device that divides a frequency of said drive clock;

an A/D converting device that converts an image signal outputted from said imaging device according to said drive clock into a digital image data;

a selection device that selectively inputs one of said drive clock and a frequency-divided clock outputted from said frequency dividing device to a signal processing part;

said signal processing part that captures said digital image data outputted from said A/D converting device in synchronization with the one of said drive clock and said frequency-divided clock outputted from said frequency dividing device, and that processes said digital image data;

a display that displays an image according to an image data outputted from said signal processing part to which said digital image data is inputted; and

a recording device that records said image data outputted from said signal processing part to which said digital image data, outputted from said A/D converting device to which said image signal is outputted from said imaging device in response to an imaging start command signal, is inputted in synchronization with said drive clock,

wherein the image data is thinned out upon ~~the input of the data into~~ application of the frequency-divided clock signal to the signal processing part.

Claim 7. (Previously Presented)

The image processing unit as set forth in claim 1, further comprising:

a horizontal drive pulse for driving a horizontal transfer passage of the imaging device, the horizontal drive pulse being divided by the frequency dividing device,

wherein the image data in a horizontal direction is thinned upon application of the divided clock to the signal processing part.

Claim 8. (Previously Presented)

The image processing unit as set forth in claim 6, further comprising:

a horizontal drive pulse for driving a horizontal transfer passage of the imaging device, the horizontal drive pulse being divided by the frequency dividing device,

wherein the image data in a horizontal direction is thinned upon application of the divided clock to the signal processing part.